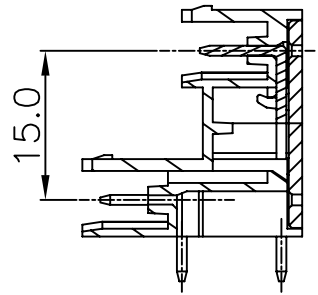
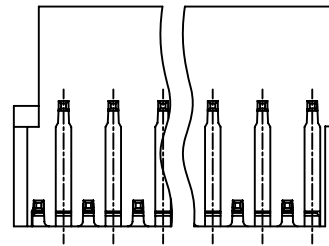
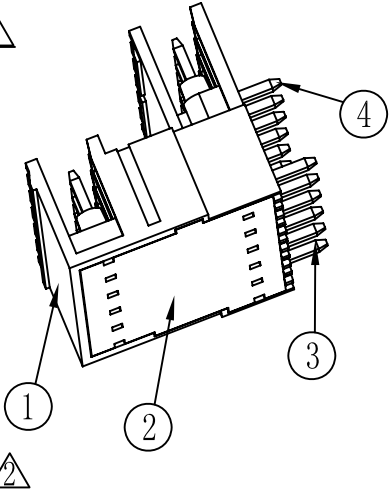
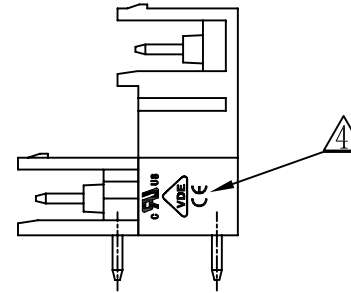
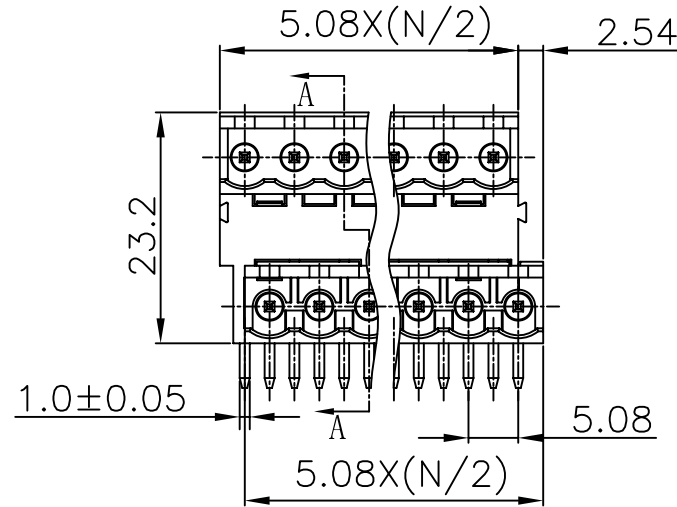
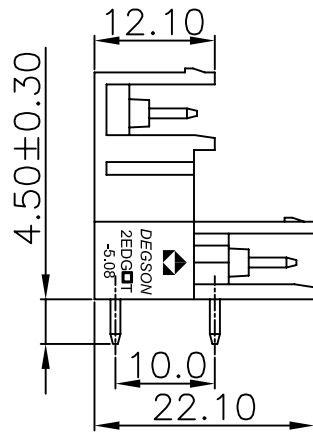
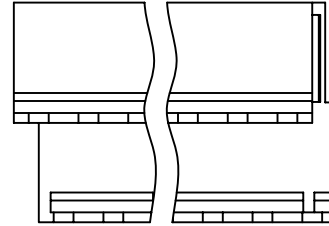


REVISION RECORD					
REV	ECO	DESCRIPTION	DRFT	DATE	
△		Drawing emend	CSX	05/06/06"	
△		1008B00040 → 1061B00030	Ye j	02/05/07"	
△		品名、料号增加环保标示“(H)”	LJH	12/11/07"	
△		增加VDE刻字	WLM	11/04/08"	
△		"4-24P"修改为"4-48P"	WLM	01/09/09"	



SECTION A-A



Notes:

1. Pitch:5.08mm , poles:4-48P (Even) △
2. Rated voltage/current:UL 300V/15A
VDE 320V/10A
3. Operation temperatures:-40℃~+105℃
4. Withstanding Voltage:AC2000V/1min
5. Insulation Resistance:5000MΩ/DC1000V
6. Undimensioned Tolerances: ▽

Pitch range in mm			Nominal dimension range in mm							
	over 6	over 10		over 30	over 60	over 100	over 150			
to 6	to 10	to 24	to 30	to 60	to 100	to 150		▽		
±0.10	±0.15	±0.25	±0.2	±0.3	±0.5	±0.7	±1.0	0.05/10		

ITEM	NAME OF PART	NO. OF PART	Q"TY	MATERIAL	NOTES
4	2EDGRH-short pin	1061B00030 (H)	XX/2	Brass	SnCe
3	2EDGRT-long pin	1032B00020 (H)	XX/2	Brass	SnCe
2	2EDGVT-5.08 cover	1056COYXXA (H)	1	PA66	UL94V-0
1	2EDGVT-5.08 housing	1056AOYXXA (H)	1	PA66	UL94V-0

DETACHED LISTS	MM (INCH)	DFTO Haoyh	DATE 04/12/05"	宁波高正电子有限公司 DEGSON ELECTRONICS CO., LTD
	TOLERANCES EXCEPT AS NOTED	CHKD	DATE	
		MFO	DATE	
		APPVL	DATE	
	NAME	2EDGRT-5.08-XXP-1Y-00A(H)		
	ANGLES ± 1°	G.E :	DRAWING NO. 2EDGRT-5.08-E01	SIZE A3
	THIRD ANGLE PROJECTION	FINISH :	/PART NO.	REV T5 01/09/09"
		SCALE : 1:1	DO NOT SCALE DRAWING	SHEET 1 OF 1